

**Remarks/Arguments**

Claims 1-15 are pending.

Indication of allowability of claims 5-7 is appreciated.

According to the Office Action, claims 12-14 are rejected under 35 U.S.C. §103(a) as being unpatentable over US Publication 2003/0020413 ("Oomura") in view of European Patent EP1221686 ("Pae").

Applicant respectfully disagrees that the combination of references teaches or even suggests each and every feature recited in claim 12, from which claims 13-14 depend.

Applicant's claim 12 includes, in part, the features of: applying a voltage to the drive transistor to drive a current through the display element, the current being drawn from a current-measurement supply line; processing the current using feedback control circuitry outside the array of pixels and having an input representing the desired current .....using the processed current, thereby implementing a feedback control loop which reaches equilibrium when the current corresponds to the desired current, and supplying the control voltage to the pixel.

Emphasis added.

The Office action admits that Oomura does not teach implementing a feedback control loop which reaches equilibrium when the current corresponds to the desired current. The Office turns to Pae paragraphs 0035, 0040 and 0041.

However, neither of Oomura or Pae shows at least the above mentioned features. In Pae columns 5 and 6 and Figs. 2-4 it is clear that the feedback proposed in Pae has no relation at all to a current through the display element. Pae in combination with Oomura fails to provide any suggestion to one skilled in the art of applicant's claimed features.

Pae shows the deviation compensator 20 detects the current from the drive transistor P0, while P2 is turned off. As described in Pae, paragraphs 0044-0052, when P2 is turned off and P3 is on, deviation compensator 20 detects the driving current of P0 by the transistor P3. Since P2 is off and P3 is on the current is not through the display element. In contrast, applicant's claimed invention recites that the current processed by feedback control circuitry is current through the display element.

Paragraphs 0039, 0052 and 0053 of Pae describe that P0 is connected to the emitting pixel OEL by the transistor P2 and the emitting pixel OEL is emitting light when P1 and P2 are turned on and the transistor P3 is turned off. Again, when P3 is turned on the current is not

through the emitting pixel OEL. Therefore, Poe's proposed feedback loop is completely different from the claimed invention.

Consequently, even if the two references are combined (Applicant's representative does not concede that Oomura and Pac are combinable, but simply states the above for the sake of argument), the combination would still fail to teach or suggest all of applicant's claimed features.

At least for the above reasons, Applicant submits that the rejection of claim 12 has been overcome and can no longer be sustained. Applicant respectfully requests withdrawal of the rejection and allowance of the claim. Dependent claims 13-14 include at least the above mentioned distinguishing features, plus additional features, and are likewise patentable.

According to the Office Action, claims 1-4, 8-11 and 15 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Oomura, Pac, further in view of Miyazawa (2004/0036664).

Applicant respectfully submits that the combination of references fails to teach or even suggest the features in claim 1 including in part: a second switch for routing current from a current-measurement supply line to the display element, ... wherein a feedback system is provided between the current-measurement supply line and the control line. Emphasis added.

The Office action admits that Oomura does not teach a feedback system provided between the current-measurement supply line and the control line. The Office turns to Pac paragraphs 0035, 0040 and 0041. Applicant's representative respectfully disagrees.

According to applicant's claim 1 the current is routed from the current measurement supply line to the display element. In contrast, and as pointed out above, Pac shows the deviation compensator 20 detects the current from the drive transistor P0, while P2 is turned off and P3 is on. Thus, deviation compensator 20 detects the driving current of P0 by the transistor P3. Since P2 is off and P3 is on the current is not to the display element. Thus Poe does not suggest the feedback system as claimed by applicants.

As a result, the combination of Oomura, Pac and Miyazawa fails to teach or suggest all the elements of claim 1. Consequently, even if the references are combined (Applicant's representative does not concede that Oomura, Miyazawa and Pac are combinable, but simply states the above for the sake of argument), the combination would still fail to teach or suggest all of applicant's claimed features.

At least for the above reasons, Applicant submits that the rejection of claim 1 has been overcome and can no longer be sustained. Applicant respectfully requests withdrawal of the rejection and allowance of the claim.

Claims 2-4, 8-11 and 15 depend from independent claims, which have been shown to be allowable over the prior art references. Accordingly, claims 2-4, 8-11 and 15 are also allowable by virtue of their dependency, as well as the additional subject matter recited therein.

In light of these remarks, it is submitted that claims 1-15 are allowable under both 35 U.S.C. §102 and 35 U.S.C. §103. Withdrawal of these rejections is respectfully requested.

In view of the foregoing, reconsideration and allowance of all the claims are respectfully solicited. In the event there are any errors with respect to the fees for this response or any other papers related to this response, the Director is hereby given permission to charge any shortages and credit any overcharges of any fees required for this submission to Deposit Account No. 14-1270.

Respectfully submitted,

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